REMARKS

The rejection of claims 1-20 under 35 USC 102(b) as being anticipated by Kanayama 4477751 is respectfully traversed.

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Although applicant has amended claim 1 and has further amended claim 9 to clarify the difference between the subject invention and the teaching in Kanayama the rejection under 35 USC 102 is misplaced since the teaching of Kanayama does not disclose a second brake circuit as is required in both claim 1 and 9 respectively. The teaching in Kanayama is limited only to the background of the invention as explained in the subject application.

In the present application the second brake control circuit corresponds to the current draw circuit 33 shown in Figure 5. As specified in claim 1 the second brake control circuit is connected to the base of the first transistor so as to cause the first transistor to be turned off by drawing a current from the base of the first transistor in accordance with the brake operation instruction signal independent of the first brake control circuit. There is no such second brake control circuit shown or taught in Kanayama nor does Kanayama suggest the use of second brake control circuit. The purpose of the second brake control circuit is to reduce the time period during which the first and second transistors are turned on.

In like manner claim 9 has been amended to also clarify the operation of the second control circuit which functions independent of the operation of the first brake control circuit and before the first transistor is turned off by the first brake control circuit in accordance with the brake operation signal. This is an entirely different method from that taught in Kanayama which does not even teach a second brake control circuit much less one that operates to turn off the first transistor independent of the first brake control circuit.

For all the above reasons the rejection of claims 1-20 under 35 USC 102(b) as being anticipated by Kanayama should be withdrawn.

Reconsideration and allowance of claims 1-20 is respectfully solicited.

Respectfully submitted,

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MAILING CERTIFICATE

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on March 2, 2006.

Date: March 2, 2006